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Search history:

No.	Database	Search term	Info added since	Results	
1	INZZ	processor OR microprocessor	unrestricted	124396	show titles
2	INZZ	checksum ADJ check ADJ sum ADJ check ADJ sequence ADJ hash ADJ FCS ADJ HMAT ADJ MIC ADJ integrity ADJ check ADJ code ADJ parity ADJ bit\$1 ADJ md4 ADJ md5 ADJ message ADJ digest ADJ error ADJ correct\$4	unrestricted	0	-
3	INZZ	checksum OR check ADJ sum	unrestricted	360	show titles
4	INZZ	check ADJ sum	unrestricted	62	show titles
5	INZZ	integrity ADJ (check OR code)	unrestricted	59	show titles
6	INZZ	CRC	unrestricted	1933	show titles
7	INZZ	1 AND 6	unrestricted	61	show titles
8	INZZ	register\$1	unrestricted	21831	show titles
9	INZZ	instruction\$1 OR opcode\$1 OR upcode\$1 OR operation\$1	unrestricted	504260	show titles
10	INZZ	7 AND 8 AND 9	unrestricted	2	show titles
11	INZZ	1 AND 8 AND 9 AND 2	unrestricted	0	-
12	INZZ	1 AND 8 AND 9 AND 4	unrestricted	0	-
13	INZZ	1 AND 8 AND 9 AND 5	unrestricted	0	-
14	INZZ	5 AND 8	unrestricted	0	-
15	INZZ	9 AND 5	unrestricted	9	show titles
16	INZZ	9 AND 6	unrestricted	137	show titles
17	INZZ	9 AND 6 AND 8	unrestricted	9	show titles
18	INZZ	10 OR 17	unrestricted	9	show titles

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(YYYYMMDD)[search](#)

	Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
√	L5	64	713/200.ccls. and ((microprocess\$3 process\$3) with (instruction\$1 operation\$1)) same((microprocess\$3 process\$3) with(checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/07 11:09
√	S1	10	("6415394" "6128528" "6092229" "5412671" "5506958" "5193181" "5291496" "5325375" "4641310" "4410984").pn.	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:59
	S2	0	(902/25 902/26 902/27 902/28 902/29 902/30).pn. and (902/38).pn.	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:28
	S3	1	"4727544".pn.	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:30
	S4	596	"714"/\$.ccls. and "712"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:31
	S5	0	instruction\$1 same final same initial same check adj sum	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:32
	S6	2091	instruction\$1 and check adj sum	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:32
	S7	184	instruction\$1 same check adj sum	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:33
√	S8	10	instruction\$1 same check adj sum with (match equal "same")	US-PGPUB; USPAT	OR	OFF	2005/07/03 12:33
√	S9	6	clock adj cycle\$ same (overshot undershot)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:01
	S10	1820	count\$3 near2 clock adj cycle\$	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:02
√	S11	16	count\$3 near2 clock adj cycle\$ with error	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:21
	S12	10468	(micro adj processor processor) same cycle\$ same instruction\$	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:27
	S13	1261	(micro adj processor processor) same cycle\$ same instruction\$ same interrupt\$1	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:22
	S14	444	(micro adj processor processor) with cycle\$ with instruction\$ same interrupt\$1	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:22
	S15	208	(micro adj processor processor) with cycle\$ with instruction\$ with interrupt\$1	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:23

S16	10	(micro adj processor processor) with cycle\$ with instruction\$ with interrupt\$1 with (stop\$3 end\$3)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:23
S17	15331	(compar\$ check\$ verif\$3) with (checksum check adj sum)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:28
S18	145	(compar\$ check\$ verif\$3) with (checksum check adj sum) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:28
S19	50	(compar\$ check\$ verif\$3) with (checksum check adj sum) same (register\$1 CPU processing adj unit) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:31
✓ S20	12	(compar\$ check\$ verif\$3) with (checksum check adj sum) same (CPU processing adj unit) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:41
S21	18	(compar\$ check\$ verif\$3) with (CRC) same (CPU processing adj unit) and "712"/\$.ccls. not (S20)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:43
✓ S22	4	(compar\$ check\$ verif\$3) with (CRC) with (CPU processing adj unit) and "712"/\$.ccls. not (S20)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:56
S23	200	((CPU processing adj unit) near3 (CRC check adj sum check sum)) near3 (calculat\$ deriv\$)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:57
S24	192	((CPU processing adj unit) near3 (CRC check adj sum check sum)) near2 (calculat\$ deriv\$)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:57
S26	84	((CPU processing adj unit) near2 (CRC check adj sum check sum)) near2 (calculat\$ deriv\$)	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:57
S27	56	((CPU processing adj unit) near2 (CRC check adj sum check sum)) near2 (calculat\$ deriv\$)and (@ad<"19990423" or @rlad<"19990423" or @prad<"19990423")	US-PGPUB; USPAT	OR	OFF	2005/07/03 13:58
✓ S28	11	((CPU processing adj unit) near2 (CRC check adj sum check sum)) near2 (calculat\$ deriv\$)and (@ad<"19990423" or @rlad<"19990423" or @prad<"19990423") and ((compar\$ check\$ verif\$3) with (checksum check adj sum))	US-PGPUB; USPAT	OR	OFF	2005/07/05 09:57

S45	84	(processor\$1 CPU\$1 microprocessor\$1 MPU\$1 ALU\$1) with (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature) with register\$1 with (instruction\$1 opcode\$ upcode\$)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:27
S46	16688	(end complet\$3 finish\$3 conclu\$3) with (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:11
S47	30018	(after end complet\$3 finish\$3 conclu\$3) with (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:10
S48	19707	(before start\$3 begin\$4 initial\$4) with (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:11
✓ S49	37	S45 and S47 and S48	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:11
S50	26143	(checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature) with (match\$3 "same" equal correlat\$3 similar\$)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:27
✓ S51	27	S49 and S50	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:22
S52	0	S49 and S50	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/06 11:22

✓	S53	10	(processor\$1 CPU\$1 microprocessor\$1 MPU\$1 ALU\$1) with (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature) with register\$1 with (instruction\$1 opcode\$ upcode\$)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/06 16:55
✓	S54	5	(processor\$1 CPU\$1 microprocessor\$1 MPU\$1 ALU\$1) with (ECC error adj correction adj code) with register\$1 with (instruction\$1 opcode\$ upcode\$)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:26
✓	S55	2	(processor\$1 CPU\$1 microprocessor\$1 MPU\$1 ALU\$1) with (ECC error adj correction adj code) with register\$1 with (instruction\$1 opcode\$ upcode\$)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/06 11:26
	S56	89	(processor\$1 CPU\$1 microprocessor\$1 MPU\$1 ALU\$1) with (ECC checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4 signature) with register\$1 with (instruction\$1 opcode\$ upcode\$)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:27
✓	S57	48	S56 and S50 not (S54 S55)	US-PGPUB; USPAT	OR	OFF	2005/07/06 11:28
	S59	1	"5504859".pn.	US-PGPUB; USPAT	OR	OFF	2005/07/06 15:09
	S65	512	(713/200 714/51 714/10 714/23 712/216).ccls. and (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4) and register\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 16:58
✓	S66	69	(713/200 714/51 714/10 714/23 712/216).ccls. and (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4) with register\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 16:58

V S67	121	(713/200 714/51 714/10 714/23 712/216).ccls. and (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4) same register\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:26
S70	63	(713/200 714/51 714/10 714/23 712/216).ccls. and (checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4) same register\$1 same (instruction\$1 operation\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:28
S71	20040	"235"/\$.ccls. and (microprocess\$3 process\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:32
S73	11697	"235"/\$.ccls. and ((microprocess\$3 process\$3) same (instruction\$1 operation\$1))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:33
S74	221	"235"/\$.ccls. and ((microprocess\$3 process\$3) same (instruction\$1 operation\$1)) same(checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:35
S75	123	"235"/\$.ccls. and ((microprocess\$3 process\$3) with (instruction\$1 operation\$1)) same(checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:35
S76	64	"235"/\$.ccls. and ((microprocess\$3 process\$3) with (instruction\$1 operation\$1)) same((microprocess\$3 process\$3) with(checksum check adj sum check adj sequence hash FCS HMAT MIC integrity adj (check code) parity adj bit\$1 md4 md5 message adj digest error adj correct\$4))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/07 11:08
V S79	127	S70 S76	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/06 17:40

File 348:EUROPEAN PATENTS 1978-2005/Jun W03

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File 349:PCT FULLTEXT 1979-2005/UB=20050623,UT=20050616

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Set	Items	Description
S1	332964	PROCESSOR? ? OR MICROPROCESSOR? ? OR (PROCESSING OR PROCES- S) () (UNIT? ? OR DEVICE? ? OR CHIP? ? OR CIRCUIT? ? OR MODULE? ? OR LOGIC OR ARCHITECTURE? ? OR SYSTEM) OR CPU? ? OR MPU? ? - OR ALU? ? OR ARITHMETIC() LOGIC() UNIT? ?
S2	112955	REGISTER? ?
S3	65236	PARITY OR CYCLIC?() REDUNDAN??() (CHECK? OR COD???) OR CRC OR FRAME() CHECK() SEQUENCE? ? OR FCS OR CHECKSUM? ? OR CHECKDIGI- T? ? OR CHECKBIT? ? OR CHECK() (SUM? ? OR DIGIT? ? OR BIT? ?) - OR BCC OR BLOCK() CHECK() CHARACTER? ? OR CONTROL() DIGIT? ?
S4	2079	(REDUNDANT OR ERROR() CORRECT? OR SYNDROME) (1W) (BIT? ? OR B- YTE? ?)
S5	911995	SECUR??? OR PROTECT??? OR GUARD??? OR SAFEGUARD??? OR INTE- GRITY OR TAMPER??? OR CORRUPT? OR MALICIOUS OR VIRUS?? OR AUT- HENTICAT? OR VERIF?
S6	1447060	INSTRUCTION? ? OR COMMAND? ? OR OPERATION? ? OR FUNCTION? ? OR PROCEDURE? ? OR EXECUT???
S7	4169	(COMPAR? OR MATCH??? OR EQUAL??? OR EQUATE? ? OR EQUATING - OR CORRESPOND? ?) (5N) S3:S4
S8	645	COMPARISON? ? (5N) S3:S4
S9	1192	S1 (30N) S2 (30N) S3:S4
S10	288	S9 (50N) S5 (50N) S6:S8
S11	1764	S2 (10N) S3:S4
S12	182	S1 (50N) S11 (50N) S5 (50N) S6:S8
S13	59	S11 (50N) S5 (50N) S6 (50N) S7:S8
S14	38	S13 AND AY=(1976:1999)/PR
S15	38	S13 AND AY=(1976:1999)
S16	38	S14:S15
S17	38	IDPAT (sorted in duplicate/non-duplicate order)
S18	201	S2 (10N) S7:S8
S19	37	S1 (50N) S18
S20	30	S19 NOT S13
S21	30	IDPAT (sorted in duplicate/non-duplicate order)

File 347:JAPIO Nov 1976-2005/Feb(Updated 050606)
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File 350:Derwent WPIX 1963-2005/UD,UM &UP=200540
(c) 2005 Thomson Derwent

Set	Items	Description
S1	863274	PROCESSOR? ? OR MICROPROCESSOR? ? OR (PROCESSING OR PROCES- S) () (UNIT? ? OR DEVICE? ? OR CHIP? ? OR CIRCUIT? ? OR MODULE? ? OR LOGIC OR ARCHITECTURE? ? OR SYSTEM) OR CPU? ? OR MPU? ? - OR ALU? ? OR ARITHMETIC() LOGIC() UNIT? ?
S2	250159	REGISTER? ?
S3	19067	PARITY OR CYCLIC?() REDUNDAN??() (CHECK? OR COD???) OR CRC OR FRAME() CHECK() SEQUENCE? ? OR FCS OR CHECKSUM? ? OR CHECKDIGI- T? ? OR CHECKBIT? ? OR CHECK() (SUM? ? OR DIGIT? ? OR BIT? ?) - OR BCC OR BLOCK() CHECK() CHARACTER? ? OR CONTROL() DIGIT? ?
S4	1156	(REDUNDANT OR ERROR() CORRECT? OR SYNDROME) (1W) (BIT? ? OR B- YTE? ?)
S5	1617898	SECUR??? OR PROTECT??? OR GUARD??? OR SAFEGUARD??? OR INTE- GRITY OR TAMPER??? OR CORRUPT? OR MALICIOUS OR VIRUS?? OR AUT- HENTICAT? OR VERIF?
S6	3460346	INSTRUCTION? ? OR COMMAND? ? OR OPERATION? ? OR FUNCTION? ? OR PROCEDURE? ? OR EXECUT???
S7	1663	(COMPAR? OR MATCH??? OR EQUAL??? OR EQUATE? ? OR EQUATING - OR CORRESPOND? ?) (5N) S3:S4
S8	56407	S1 AND S2
S9	734	S8 AND S3:S4
S10	65	S9 AND S5
S11	45	S10 AND S6:S7
S12	13	S11 AND AC=US/PR
S13	12	S12 AND AY=(1976:1999)/PR
S14	35	S11 AND PY=1976:1999
S15	40	S13:S14

File 8: Ei Compendex(R) 1970-2005/Jun W3
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 File 62: SPIN(R) 1975-2005/Apr W2
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 File 239: Mathsci 1940-2005/Aug
 (c) 2005 American Mathematical Society

Set	Items	Description
S1	632883	PROCESSOR? ? OR MICROPROCESSOR? ? OR (PROCESSING OR PROCES- S) () (UNIT? ? OR DEVICE? ? OR CHIP? ? OR CIRCUIT? ? OR MODULE? ? OR LOGIC OR ARCHITECTURE? ? OR SYSTEM) OR CPU? ? OR MPU? ? - OR ALU? ? OR ARITHMETIC () LOGIC () UNIT? ?
S2	81466	REGISTER? ?
S3	150150	PARITY OR CYCLIC? () REDUNDANT? () (CHECK? OR COD???) OR CRC OR FRAME () CHECK () SEQUENCE? ? OR FCS OR CHECKSUM? ? OR CHECKDIGI- T? ? OR CHECKBIT? ? OR CHECK () (SUM? ? OR DIGIT? ? OR BIT? ?) - OR BCC OR BLOCK () CHECK () CHARACTER? ? OR CONTROL () DIGIT? ?
S4	794	(REDUNDANT OR ERROR () CORRECT? OR SYNDROME) (1W) (BIT? ? OR B- YTE? ?)
S5	3468403	SECUR??? OR PROTECT??? OR GUARD??? OR SAFEGUARD??? OR INTE- GRITY OR TAMPER??? OR CORRUPT? OR MALICIOUS OR VIRUS?? OR AUT- HENTICAT? OR VERIF?
S6	9848029	INSTRUCTION? ? OR COMMAND? ? OR OPERATION? ? OR FUNCTION? ? OR PROCEDURE? ? OR EXECUT???
S7	4389	(COMPAR? OR MATCH??? OR EQUAL??? OR EQUATE? ? OR EQUATING - OR CORRESPOND? ?) (5N) S3:S4
S8	0	S1 AND S2 AND S3:S4 AND S5 AND S6 AND S7
S9	105	S1 AND S2 AND S3:S4
S10	8	S9 AND S5 AND S6:S7
S11	13	S9 AND S5
S12	2	S9 AND S7
S13	50	S9 AND S6
S14	28	S9 AND (INSTRUCTION? ? OR EXECUT???)
S15	38	S10:S12 OR S14
S16	27	RD (unique items)
S17	21	S16 NOT PY=2000:2005

File 275:Gale Group Computer DB(TM) 1983-2005/Jun 29
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File 621:Gale Group New Prod.Annou.(R) 1985-2005/Jun 29
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File 636:Gale Group Newsletter DB(TM) 1987-2005/Jun 29
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File 16:Gale Group PROMT(R) 1990-2005/Jun 29
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File 160:Gale Group PROMT(R) 1972-1989
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(c) 2005 The Dialog Corp.
File 369:New Scientist 1994-2005/May W1
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Set	Items	Description
S1	1365409	PROCESSOR? ? OR MICROPROCESSOR? ? OR (PROCESSING OR PROCES- S)() (UNIT? ? OR DEVICE? ? OR CHIP? ? OR CIRCUIT? ? OR MODULE? ? OR LOGIC OR ARCHITECTURE? ? OR SYSTEM) OR CPU? ? OR MPU? ? - OR ALU? ? OR ARITHMETIC()LOGIC()UNIT? ?
S2	547525	REGISTER? ?
S3	123417	PARITY OR CYCLIC?()REDUNDAN??() (CHECK? OR COD???) OR CRC OR FRAME()CHECK()SEQUENCE? ? OR FCS OR CHECKSUM? ? OR CHECKDIGI- T? ? OR CHECKBIT? ? OR CHECK() (SUM? ? OR DIGIT? ? OR BIT? ?) - OR BCC OR BLOCK()CHECK()CHARACTER? ? OR CONTROL()DIGIT? ?
S4	468	(REDUNDANT OR ERROR()CORRECT? OR SYNDROME) (1W) (BIT? ? OR B- YTE? ?)
S5	6393396	SECUR??? OR PROTECT??? OR GUARD??? OR SAFEGUARD??? OR INTE- GRITY OR TAMPER??? OR CORRUPT? OR MALICIOUS OR VIRUS?? OR AUT- HENTICAT? OR VERIF?
S6	13004647	INSTRUCTION? ? OR COMMAND? ? OR OPERATION? ? OR FUNCTION? ? OR PROCEDURE? ? OR EXECUT???
S7	2107	(COMPAR? OR MATCH??? OR EQUAL??? OR EQUATE? ? OR EQUATING - OR CORRESPOND? ?) (5N) S3:S4
S8	12	S2(10N) S7
S9	15	S2(15N) S7
S10	11	RD (unique items)
S11	396	S2(10N) S3:S4
S12	96	S1(50N) S11
S13	32	S1(50N) S11(50N) S5
S14	17	RD (unique items)
S15	12	S14 NOT (S10 OR PY=2000:2005)